



PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Manrique J. Brenes and Yen T. Nguyen
Assignee: Cisco Technology, Inc.
Title: METHOD AND SYSTEM FOR REAL-TIME BIT
ERROR RATIO DETERMINATION
Application No.: 10/773,705 Filing Date: February 6, 2004
Examiner: Unassigned Group Art Unit: 2133
Docket No.: CIS0209US Confirmation No.: 8310

COMMISSIONER FOR PATENTS
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POWER OF ATTORNEY
BY ASSIGNEE OF ENTIRE INTEREST

Sir:

The above-identified assignee, a California corporation, hereby revokes all powers of attorney previously given and appoints the attorney(s) and/or agent(s) identified by the following attorneys to prosecute the above-identified application and to transact all business in the United States Patent and Trademark Office in connection therewith:

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ASSIGNEE CERTIFICATION UNDER 37 CFR 3.73(B)

The undersigned representative of the above-identified assignee certifies that the above-identified assignee is the assignee of the entire right, title and interest in the above-identified patent application by virtue of a chain of title from the inventor(s) of the above-identified patent application to the above-identified assignee as shown below:

The document was recorded in the Patent and Trademark Office at Reel 014974, Frame 0104.

The undersigned (whose title is supplied below) is empowered to sign this certificate on behalf of the above-identified assignee.

DEC 17 2004

Date


ROBERT BARR

Vice President of Intellectual Property
and Worldwide Patent Counsel